

What is claimed is:

Sub 26
1. ~~A driving circuit of a display device for displaying~~
a plurality of gray shades based on inputted digital image data
comprising:

gray shade voltage generating means for generating a
plurality of voltages;

gray shade voltage selecting means for selecting one
voltage out of a plurality of voltages supplied from said gray
shade voltage generating means based on high order bits composed
of one or two and more bits counted from the most significant
bit of said digital image data and the number of bits of which
is smaller than that of said digital image data, and for outputting
said voltage;

an operational amplifier used to convert an impedance of
a voltage outputted from said gray shade voltage selecting means;
and

voltage adjusting means for inducing a voltage rise or
a voltage drop of a voltage outputted from said operational
amplifier based on low order bits of said digital image data
excluding said high order bits.

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2. ~~The driving circuit of the display device according~~
to claim 1, wherein said voltage adjusting means is comprised
of a resistor connected to an output terminal of said operational
amplifier, an active device connected to said resistor and
controlling means for controlling operations of said active
device based on said low order bits.

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3 when values between adjacent gray shade voltages are not equal,
4 is used to select one voltage out of a plurality of voltages
5 supplied from said gray shade voltage generating means based
6 on all bits of said digital image data and to output said voltage.

1 11. The driving circuit of the display device according
2 to claim 9, wherein said gray shade voltage generating means
3 is provided with two or more input terminals to which an voltage
4 is inputted from outside and with dividing means used to divide
5 voltages inputted into said input terminals into many voltages.

1 12. The driving circuit of the display device according
2 to claim 9, wherein a voltage outputted from said gray shade
3 voltage generating means is a positive polarity voltage or a
4 negative polarity voltage.

1 13. The driving circuit of the display device according
2 to claim 9, wherein, when the number of bits of said digital
3 image data is N, said high order bits are composed of (N-m) bits
4 counted from the most significant bit of said digital image data
5 and said low order bits are composed of m bits counted from the
6 least significant bit of the digital image data.

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